

10/679124 -

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	<u>Not yet assigned.</u>
		Filing Date	<u>Herewith 10/2/2003</u>
		First Named Inventor:	<u>Suman Datta et al.</u>
		Art Unit	<u>Newly assigned B81</u>
		Examiner Name	<u>Not yet assigned Itma</u>

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	Ivan Hwang	Date Considered	5/15/05
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**INFORMATION DISCLOSURE
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Sheet

2

of

2

Complete if Known

Application Number	Not yet assigned 10/679124 ✓
Filing Date	March 10/02/2003 ✓
First Named Inventor:	Suman Datta et al.
Art Unit	Not yet assigned 2827 ✓
Examiner Name	Not yet assigned Thuan Hoang

Attorney Docket Number 42P16682

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
TH		Evert Seevinck, "Static-Noise Margin Analysis of MOS SRAM Cells", IEEE Journal of Solid State Circuits, Vol. Sc-22, No. 5, October 1987, pp 748-754	
TH		Peter A. Stolk et al., "Modeling Statistical Dopant Fluctuations in MOS Transistors", IEEE Transactions on Electron Devices, Vol. 45, No. 9, September 1998, pp. 1960-1971	
checked		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/227,068, Filed August 23, 2002	
checked		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/367,263, Filed February 14, 2003	

Examiner Signature	Thuan Hoang	Date Considered	5/15/05
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